# Detail requirements:

* Powered by USB 2.0
* Include 1-8 MHz clock
* Have pseudo random pattern generator (LSFR)
* Capable of injecting other signals external function generator.
  + External clock can be used
  + LSFR can be disconnected and other patters used
* Will have 1 victim with 2 aggressors at each side of victim.
* Include Test points near input and output
* Will include variable passive components (R,C,L) to affect transmission line properties
* User must be able to perform experiment using USB powered board and oscilloscope

# Description

Before completing this experiment is recommended that the student completes and understands the crosstalk and the Intersymbol interference (ISI) experiment in the E3VB. ISI distortion is a result of previous symbols interfering with the current symbol. Crosstalk is a form of signal distortion that happens when the nearby traces interfere with each other. Both factors may affect signals at the same time; our focus is to provide an experiment that demonstrates the effect of both ISI and crosstalk either independently or simultaneously.

The new ISI experiment consists of expanding and improving both the ISI and the crosstalk experiment on the E3VB. To allow the board to be used in industry and in academic settings it does provide everything needed to complete the experiment build into the board (clock and pseudo random function generator). If the user wants to make different observation it can inject a new clock or digital patterns by disabling the built-in components and using provided jumpers.